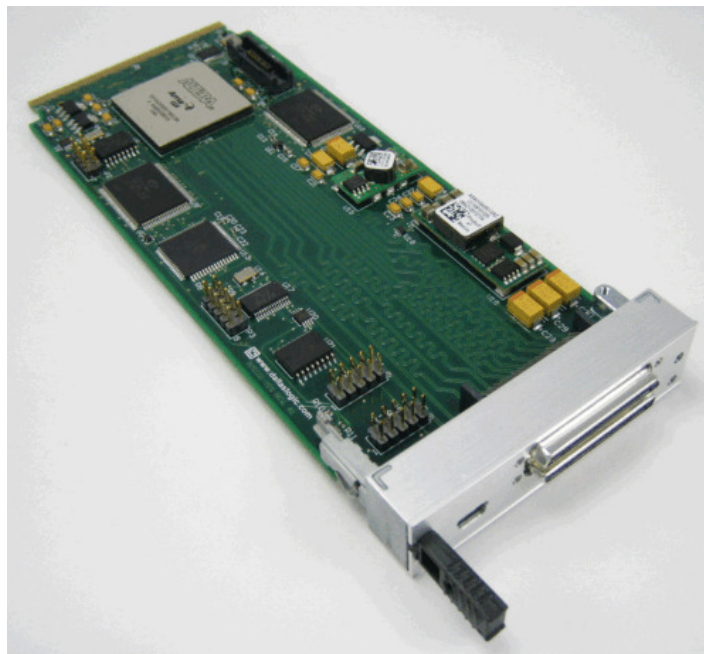


FPGA based LVDS Interface AMC

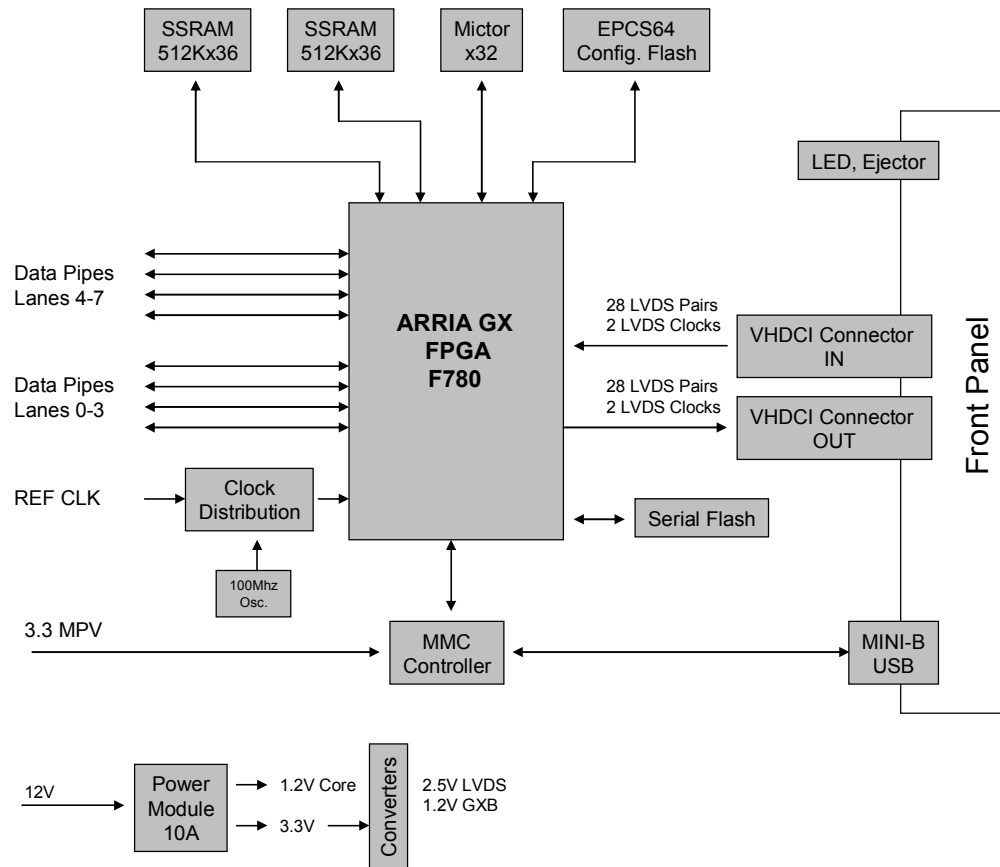
The A01-LVDS Interface AMC (Advanced Mezzanine Card), is a fully integrated FPGA based LVDS transceiver card. The design features Altera's™ ARRIA® GX FPGA in the F780 BGA package. The backplane interface is user configurable to support several interface standards including: PCI Express, Serial Rapid IO, and Gigabit Ethernet. The front panel VHDCI connector supports 28 transmit and 28 receive LVDS links sourced from the FPGA. Additional features include: two 512Kx36 synchronous SRAMs, an IPMI 1.5 compliant MMC (Module Management Controller), a 32Mbit serial flash, two onboard temperature sensors, USB communication / debug interface, and a 32bit Mictor debug connector.

Features:

- Altera™ ARRIA® EP1AGX FPGA (BGA F780)
- Backplane interface support for up to two x4, four x2 and eight x1 data pipes.
- Configurable backplane interface, capable of supporting PCI-E, GigE, and SRIO.
- Two 512Kx36 SSRAMs.
- IPMI 1.5 compliant Module Management Controller.
- USB mini-B type connector for terminal / debug interface.
- 32 Mbit serial flash.
- Two temperature sensors.
- Mictor Debug connector.
- Dual 68 pin VHDCI connector supporting 28 TX and 28 RX LVDS signals.
Data rates up to 800 Mbps.
- AMC mid-size or full-size, single width format, compatible with AMC.0 specification R2.0



FPGA based LVDS Interface AMC



Ordering Information:

Part Number	Altera FPGA Device	Description
A01-LVDS-35-C-M	EP1AGX35DF780C6N	Commercial grade FPGA, 1AGX35, -6 speed grade, Mid-size faceplate
A01-LVDS-35-C-F	EP1AGX35DF780C6N	Commercial grade FPGA, 1AGX35, -6 speed grade, Full-size faceplate
A01-LVDS-50-C-M	EP1AGX50DF780C6N	Commercial grade FPGA, 1AGX50, -6 speed grade, Mid-size faceplate
A01-LVDS-50-C-F	EP1AGX50DF780C6N	Commercial grade FPGA, 1AGX50, -6 speed grade, Full-size faceplate
A01-LVDS-60-C-M	EP1AGX60DF780C6N	Commercial grade FPGA, 1AGX60, -6 speed grade, Mid-size faceplate
A01-LVDS-60-C-F	EP1AGX60DF780C6N	Commercial grade FPGA, 1AGX60, -6 speed grade, Full-size faceplate